

Claims

[c1] What is claimed is:

- 1.A method for driving an organic light emitting diode (OLED), the method comprising:
 - (a) providing a first metal oxide semiconductor (MOS) transistor, whose first and second ends are connected to the OLED and to a first voltage source respectively;
 - (b) providing a capacitor, whose first end is connected to a gate of the first MOS transistor;
 - (c) providing a second MOS transistor, whose first end is utilized for inputting data, a second end of the second MOS transistor being connected to the first end of the capacitor;
 - (d) turning on the second MOS transistor and inputting data from the first end of the second MOS transistor to the second end of the second MOS transistor; and
 - (e) turning off the second MOS transistor after step (d), and adjusting a voltage at a second end of the capacitor from a first voltage level to a second voltage level different from the first voltage level sequentially.

[c2] 2.The method of claim 1, wherein the first voltage level is lower than the second voltage level.

- [c3] 3.The method of claim 1, wherein the first voltage level is greater than the second voltage level.
- [c4] 4.The method of claim 1, wherein step (e) comprises: after the voltage at the second end of the capacitor has been adjusted to a voltage level equal to the second voltage level, adjusting the voltage at the second end of the capacitor to a voltage level equal to the first voltage level again.
- [c5] 5.The method of claim 1, wherein the first MOS transistor is a thin film transistor (TFT).
- [c6] 6.The method of claim 1, wherein the first MOS transistor is a PMOS transistor.
- [c7] 7.The method of claim 1, wherein the first MOS transistor is an NMOS transistor.
- [c8] 8.An OLED driving circuit comprising:
 - an OLED having a first end connected to a first voltage source;
 - a first MOS transistor having a first end connected to a second end of the OLED and a second end connected to a second voltage source;
 - a second MOS transistor having a first end connected to a gate of the first transistor, a second end for inputting

data, and a gate for inputting a select signal; and a capacitor having a first end connected to the first end of the second MOS transistor and a second end connected to a reference voltage.

- [c9] 9.The circuit of claim 8, wherein the first MOS transistor is a TFT.
- [c10] 10.The circuit of claim 8, wherein the first MOS transistor is a PMOS transistor.
- [c11] 11.The circuit of claim 8, wherein the first MOS transistor is an NMOS transistor.